

Customer No.: 31561
Docket No.: 13321-US-PA
Application No.: 10/711,472

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In The Specification:

Please amend paragraph [Para 5] as follows:

[Para 5] In addition, in order to follow the right trend of intensive circuitry and fulfill the requirement of reducing the product development time, how to develop a most economical chip product has become a major topic for development engineers now, and reusing of the circuitry design is one of the techniques to achieve this object. In other words, by applying this technique, various functional circuitries which are well designed layout can be repeatedly used to build up a new Application Specific Integrated Circuit (ASIC). Therefore, the development process of the IC product can be completed in a very short time period. Here, the well designed circuitry layout that can be repeatedly used is also known as an IP (Intellectual Property) component.